

Documentation & Tutorials

Getting started on Iridium

Copy example applications

```
cd ~/scratch
cp -r /opt/Xilinx/SDx/2017.1/examples .
```

Compile hello world example

```
source /opt/Xilinx/SDx/2017.1/settings64.sh

cd ~/scratch/examples/hello_world
# edit sdaccel.mk file to make XDEVICE=xilinx:kcu1500:4ddr-xpr:4.0

# compile host executable
make -f sdaccel.mk host
```

Compile and run on the actual hardware	Compile and run hardware emulation
<pre># compile hardware - this will take time! make -f sdaccel.mk xbin_hw # setup environment source /opt/Xilinx/kcu1500/xbinst/setup.sh # run on FPGA ./host bin_vadd.xclbin</pre>	<pre>make -f sdaccel.mk run_hw_em</pre>

and check performance results in sdaccel_profile_summary.html and sdaccel_profile_summary.csv files.

Read README.md for other options.

Xilinx Documentation

SDK documentation: <https://www.xilinx.com/products/design-tools/software-zone/sdaccel.html>

Tutorials: https://www.xilinx.com/html_docs/xilinx2017_2/sdaccel_doc/index.html Examples: https://github.com/Xilinx/SDAccel_Examples

- **UG1023** - [SDAccel Environment User Guide \(ver2017.2\)](#)
- **UG1021** - [SDAccel Environment Tutorial: Introduction \(ver2017.2\)](#)

Development documentation

- **UG1207** - [SDAccel Environment Optimization Guide \(ver2017.2\)](#)
- **UG1253** - [SDx Pragma Reference Guide \(ver2017.2\)](#)

Hardware-related

- **UG1238** - [SDx Environments Release Notes, Installation, and Licensing Guide \(ver2017.2\)](#)
- **UG1164** - [SDAccel Environment Platform Development Guide \(ver2017.2\)](#)
- **UG1234** - [SDAccel Platform Reference Design User Guide: Developer Board for Acceleration with KCU1500 \(ver2017.2\)](#)