AMD EPYC™ 7xx3-series Processors





AOCC compiler (C/C++/Fortran)

Latest release: 3.2, December 2021 https://developer.amd.com/amd-aocc/

Architecture		
Generate instructions that runs on AMD 3 rd Gen EPYC TM and AMD 3 rd Gen Ryzen TM	-march=znver3	
Generate instructions for the local machine	-march=native	
Optimization Levels		
Disable all optimizations	-00	
Minimal level speed and code optimization	-01/-0	
Moderate level optimization	-02	
Aggressive optimizations	-03	
Maximize performance	-Ofast	
Enable link time optimization	-flto	
Enable loop optimizations	-funroll-loops -enable-licm-vrp -enable-partial-unswitch -fuse-tile-inner-loop -unroll-threshold	
Enable advanced loop optimizations	-unroll-aggressive	
Enable function level optimizations	-fitodcalls -function-specialize -finline-aggressive -inline-recursion={14} (use with flto) -do-block-reordering={none, normal, aggressive}	
Enable advanced vectorization	-enable-strided-vectorization -enable-epilog-vectorization	
Enable memory layout optimizations	-fremap-arrays (use with -flto)	
Profile guided optimizations	-fprofile-instr-generate (1st invocation) -fprofile-instr-use (2nd invocation)	
OpenMP®	-fopenmp	
For enabling streaming stores, memory bandwidth workloads	-fnt-store	
Enable removal of un-used array computation	-reduce-array-computations=3	

Other options		
Enable faster, less precise math operations (part of Ofast)	-ffast-math -freciprocal-math	
OpenMP® threads and affinity (N number of cores)	export OMP_NUM_THREADS=N export GOMP_CPU_AFFINITY="0- {N-1}"	
Enabling vector library	-fveclib=AMDLIBM	
Link to AMD library	-L/libm-install-dir/lib -lalm	

For Fortran Workloads		
	Compile free form FORTRAN	-ffree-form

AMD Optimized Libraries

Latest release: 3.1, Dec 2021 https://developer.amd.com/amd-aocl/

AMD µProf (Performance & Power Profiler)

Latest release: 3.4, March 2021 https://developer.amd.com/amd-uprof/

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Compiler Options Quick Reference Guide



GNU compiler collection

Latest release: 11.2, July 2021

Recommended version: GCC trunk, later than 5-Dec-2021

http://gcc.gnu.org

Microsoft® Visual Studio 2022

Latest release: 17.0.1.2, November 2021

https://visualstudio.microsoft.com/

User Guide

Architecture		Arc
Generate instructions that runs on AMD 3 rd Gen EPYC TM and AMD 3 rd Gen Ryzen TM	-march=znver3	Ger on A
Generate instructions for the local machine	-march=native	Opt
Optimization Levels		Opt
Disable all optimizations (default)	-00	Disa
Minimal level speed and code optimizations	-01/-0	spa
Moderate level optimizations	-02	Max
Aggressive optimizations	-03	[linl
Maximize performance	-Ofast	enc
Additional Optimizations		[linl
Link time optimization	-fito	COI
Enable unrolling	-funroll-all-loops	Out
Generate memory preload instructions	-fprefetch-loop-arraysparam prefetch-latency=300	vec
Profile-guided optimization	-fprofile-generate (1st invocation) -fprofile-use (2nd invocation)	ena of lowith
OpenMP®	-fopenmp	Out
Other options S		sage
Enable generation of code that follows IEEE arithmetic	-mieee-fp	Add
Enable faster, less precise math operations	-ffast-math	Mai
Compile free form FORTRAN	-ffree-form	thro
OpenMP® threads and affinity (N number of cores)	export OMP_NUM_THREADS=N export GOMP_CPU_AFFINITY="0-{N-1}"	Opt spe -poi
Link to AMD library	-L/libm-install-dir/lib -lalm	
		Wh

Architecture		
Generate instructions that runs on AMD 3^{rd} Gen EPYC TM and	/arch:[AVX AVX2]	
Optimize for 64-bit AMD processors	/favor:AMD64 /d2vzeroupper	
Optimization Levels		
Disable optimizations	/Od	
Maximum optimizations (favor space)	/01	
Maximum optimizations (favor speed)	/02	
[link.exe] Eliminate unreferenced function and/ or data	/OPT:REF	
[link.exe] Perform identical COMDAT folding	/OPT:ICF	
Output an informational message for loops that are autovectorized	/Qvec-report:[1 2]	
Enable automatic parallelization of loops, used in conjunction with #pragma loop() directive	/Qpar	
Output an informational message for loops that are autoparallelized	/Qpar-report:[1 2]	
Additional Optimizations		
Maintain the precision for floating-point operations through proper rounding	/fp:precise	
Optimize floating-point code for speed at the expense of floating -point accuracy and correctness	/fp:fast	
Whole Program Optimization (link-time code generation)	/GL	
Profile-guided optimization	LTCG:PGI and /LTCG:PGO	

GlibC

Latest release: 2.34, August 2021 Recommendation: 2.26 or later https://www.gnu.org/software/libc/

Binutils

Recommendation: 2.26 or later https://www.gnu.org/software/binutils/

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Compiler Options Quick Reference Guide



Intel® compilers

Latest release: 2021.4 http://software.intel.com

Architecture	chitecture	
Generate instructions that runs on AMD 3 rd Gen EPYC TM and AMD 3 rd Gen Ryzen TM	-march=core-avx2 (preferred) <u>OR</u> -axCORE-AVX2	
Optimization Levels		
Disable all optimizations	-00	
Speed optimization without code growth	-01	
Enable optimization for speed including vectorization	-02	
Aggressive optimization	-03	
Maximize performance	-Ofast	
Additional Optimizations		
Aggressive unrolling	-unroll-aggressive	
Disable improved precision floating divides	-no-prec-div	
Enable vectorization	-vec	
Inter procedural Optimization	-ipo	
OpenMP®	-qopenmp	
Prefetch optimization	-qopt-prefetch	
Profile generated optimization	-prof-gen and -prof-use	
Use optimized header definitions	-use-intel-optimized-headers	
Other Options		
Floating point accuracy tuning	-fp-model	
Compile free form FORTRAN	-free	

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